Performance evaluation of next-generation data center and HPC networks with co-packaged optics

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ABSTRACT

The increased escape bandwidth offered by co-packaged optics can enable switches with speeds of 51.2 Tb/s and beyond. From a network architecture perspective, there are two key advantages: (a) the implementation of large-scale topologies with significantly higher bisection bandwidth, and (b) the substantial reduction of the required number of switches, which can mitigate the administrative/management overhead. From a network operation perspective, both improved network locality and faster operation can be achieved since the higher-radix switches can reduce the impact of network contention; applications can be placed under fewer leaf switches, which reduces the number of packets that cross the spine switches in a typical leaf-spine topology. This presentation provides a brief overview of the recent activities realized within the framework of the MOTION research project (Multi-wavelength Optical Transceivers Integrated On Node), and discusses on the performance improvements that can be achieved by using co-packaged optics in next-generation data center and high-performance computing networks. The proposed concepts are evaluated via discrete-event simulations: first, virtual-machine traces are used to evaluate the network locality properties of the system, and, secondly, the performance improvements are quantified by means of network simulations with an OMNEST-based simulator.

Keywords: Network simulation, Network optimization, Co-packaged optics, Optical interconnects, Fat-tree networks.

RELATED MATERIAL

Previous papers with detailed results:

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